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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. N .	Unknown /
				Filing Date	November 21, 2003
				First Named Inventor	Bernard J. New
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	1	of	1	Attorney Docket Number	X-1141 US

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		XILINX, INC., "Using Delay-Locked Loops in Spartan-II FPGAs," Application Note: Spartan-II FPGAs, XAPP174, v1.1, January 24, 2000, pp 1-13; Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached. ³

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